

# ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

<b>Title of Invention</b>	HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BiCMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE
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Submission Type : Utility Patent Filing

Application Number:

10/707863



EFS ID:

53941

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	1862
WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font fomat.

First Named Applicant: Ebenezer Eshun

Attorney Docket Number: BUR920030058US1

Timestamp: 2004-01-19 15:34:51 EDT

From: US

File Listing:

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us-ids	BUR920030058US1-usidst.xml	2383	2004-01-19
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us-fee-sheet	us-fee-sheet.dtd	11069	2004-01-19
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